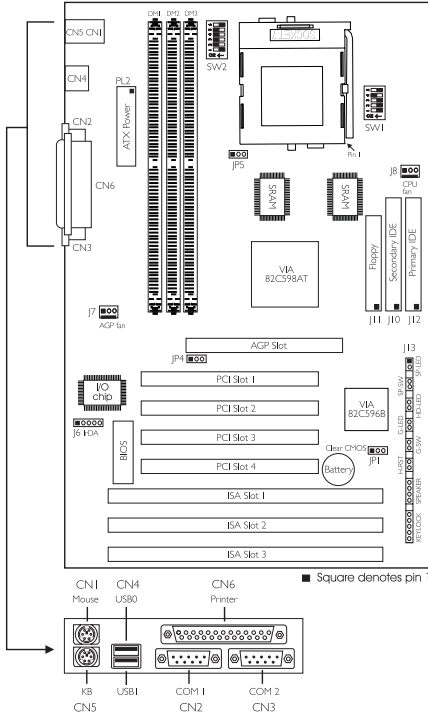


K6XV3+/66

rev. B+



Black rectangle denotes the part that is protruding, the "adjustable" switch

In the example above:

- Switch 1: Off
- Switch 2: On
- Switch 3: On
- Switch 4: On
- Switch 5: On
- Switch 6: Off

Voltage	JPS	SW1
2.1V		
2.2V		
2.3V		
2.4V		
2.5V		
2.8V*		
2.9V		
3.2V		
3.3V		
3.5V		

Please refer to the user's manual for a complete list of the voltage settings.

AMD - SW2: 1-6	AMD - SW2: 1-6	Cyrix - SW2: 1-6	IDT - SW2: 1-6
K6-2/300 - 66MHz - 4.5x	K6-2/475 - 95MHz - 5x	M II-200 - 66MHz - 2.5x	WinChip2-200 - 66MHz - 3x
K6-2/300 - 100MHz - 3x	K6-2/500 - 100MHz - 5x	M II-266 - 66MHz - 3x	WinChip2-225 - 75MHz - 3x
K6-2/333 - 66MHz - 5x	K6-2/550 - 100MHz - 5.5x	M II-300 - 66MHz - 3.5x	WinChip2-233 - 66MHz - 3.5x
K6-2/333 - 95MHz - 3.5x	K6-III/400 - 100MHz - 4x	M II-300 - 75MHz - 3x	WinChip2-266 - 100MHz - 2.33x
K6-2/350 - 100MHz - 3.5x	K6-III/450 - 100MHz - 4.5x	M II-333 - 83MHz - 3x	WinChip2-300 - 100MHz - 2.5x
K6-2/366 - 66MHz - 5.5x	K6-III/475 - 95MHz - 5x	M II-366 - 100MHz - 2.5x	
K6-2/380 - 95MHz - 4x	K6-III/500 - 100MHz - 5x	M II-400 - 95MHz - 3x	
K6-2/400 - 100MHz - 4x	K6-III/550 - 100MHz - 5.5x	M II-433 - 100MHz - 3x	
K6-2/450 - 100MHz - 4.5x			
	Note: Please refer to the user's manual for the settings of older CPUs.		

* denotes default setting

Jumper JPI (CMOS Clear) : 1-2 On: Clear CMOS Data; 2-3 On: Normal (default)
 Jumper JP4 (SDRAM Clock) : 1-2 On: SDRAM CLK = AGP CLK (default); 2-3 On: SDRAM CLK = CPU CLK